

We Claim:

- ↗ An apparatus for synchronizing a portable electronic device with a host computer, comprising:
- 5 an interface, including an interface circuit having a transmit buffer and a receive buffer and a cable configured for connection to a communication port of said host computer and to a data port of said portable electronic device;
- 10 a signal path connected between a voltage source of said portable electronic device and said interface, said signal path configured to provide electrical power to said interface circuit upon connection of said cable to said data port of said portable electronic device;
- 15 a processor in said portable electronic device, said processor having a status signal input coupled to an output of said transmit buffer, wherein said transmit buffer is configured to output a first logic level upon application of power from said signal path, said first logic level resulting in a true logic level status signal at said status signal input; and
- 20 computer program code logic, executed by said processor and configured to initiate a synchronization process with said host computer when said status signal presents a true logic level at said status signal input.
2. The system of claim 1, wherein said computer program code logic is configured to initiate said synchronization process when said 25 status signal presents a true logic level at said status signal input for a plurality of successive clock cycles.
3. The system of claim 1, wherein said transmit buffer is an inverting buffer having an input pulled to a logic low level by a pull-down

resistor and outputting a logic level high signal when power is applied in the absence of data at said transmit buffer input.

4. The system of claim 3, wherein said status signal is pulled
5 high by a pull-up resistor and said transmit buffer output is inverted to provide a logic low level status signal when power is applied to said transmit buffer in the absence of data at said transmit buffer input.

5. The system of claim 1, wherein said interface comprises a
10 cable having said interface circuitry integrated therewith.

6. The system of claim 1, wherein said interface comprises a docking station and a cable for connecting said docking station to said host computer.

15 7. The system of claim 6, wherein said interface circuit is integrated within said docking station.

8. The system of claim 6, wherein said docking station
20 includes electrical contacts configured to mate with corresponding electrical contacts on said portable electronic device.

9. The system of claim 1, wherein said portable electronic device is a wireless communication handset.

25 10. The system of claim 1, wherein said electronic device comprises computer program code logic configured to allow a user to enable or disable automatic synchronization.

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11. The system of claim 1, wherein said electronic device further comprises computer program code logic configured to determine whether said electronic device has been disconnected from and reconnected to said interface, and for initiating synchronization upon
5 reconnection.

12. In a portable electronic device, an apparatus for enabling the portable electronic device to automatically synchronize to a host computer without user intervention, upon connection to said host
10 computer, comprising:
a data port configured to connect said portable electronic device to the host computer via an interface;
a power line configured to provide power from said portable electronic device to said data port, wherein connection of said interface to said port supplies power to said interface via said power line, thereby
15 providing power to a buffer in said interface;
a processor in said portable electronic device, said processor having a status signal input coupled to an output of said buffer via said data port, wherein said buffer is configured to output a first logic level
20 upon application of power from said power line, said first logic level resulting in a true logic level status signal at said status signal input; and
computer program code logic, executed by said processor and configured to initiate a synchronization process with said host computer when said status signal presents a true logic level at said status signal
25 input.

13. The system of claim 12, wherein said computer program code logic is configured to initiate said synchronization process when said

status signal presents a true logic level at said status signal input for a plurality of successive clock cycles.

14. The system of claim 12, wherein said interface comprises
5 a docking station and a cable for connecting said docking station to said host computer, and wherein said synchronization process begins when said electronic device is placed in said docking.

15. The system of claim 12, wherein said portable electronic
10 device is a wireless communication handset.

16. The system of claim 12, wherein said electronic device comprises computer program code logic configured to allow a user to enable or disable automatic synchronization.

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17. The system of claim 12, wherein said electronic device further comprises computer program code logic configured to determine whether said electronic device has been disconnected from and reconnected to said interface, and for initiating synchronization upon
20 reconnection.

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